# EIG CONCERT JAPAN Connecting and Coordinating European Research and Technology Development with Japan



# March 23<sup>rd</sup>, 2022 09:00 - 12:30 CET, (17:00 - 20:30 JST) MS Teams (Videolink)

## Workshop program

- 09:00 Welcome and introduction to program and speakers by host / practical aspects Dr. Hans Chr Bolstad, SINTEF
- 09:15 Introduction to the topic HVDC Prof. Dr.-Ing. Lutz Hofmann, Leibniz University Hannover
- 09:30 The Modulator Project Dr. Salvatore D'Arco, SINTEF
- 09:40 A modular framework for state-space modelling of hybrid DC grids Dr. Jon Are Suul, SINTEF
- 09:55 Questions and Discussion

#### 10:10 Coffee break

- 10:20 Large-signal analysis of interconnected AC/DC systems: Theory and demonstration Prof. Yoshihiko Susuki, Osaka Prefecture University
- 10:35 Optimization considering small signal aspects Dr.-Ing. Denis Mende, Fraunhofer IEE
- 10:50 Emerging power electronics technology in HVDC systems for renewable energy Prof. Tsuyoshi Funaki, Osaka University
- 11:05 Questions and Discussion
- 11.20 Coffee break
- 11:30 Small-signal modelling for studying converter interactions in HVDC systems Prof. Jef Beerten, KU Leuven
- 11:45 HVDC System Studies Dr.-Ing. Tobias Hennig, Amprion GmbH
- 12:00 Discussion & conclusions Dr. Salvatore D'Arco, SINTEF

### The workshop is organised by the EIG-Concert Japan Project:

Module-Aware Modelling and Assessment of Performance of Interconnected AC/MTDC Power Grids (MODULATOR), SINTEF Energy Research, Osaka Prefecture University, Fraunhofer IEE, with guest speakers from Osaka University, Amprion GmbH, Leibniz University and KU Leuven.

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